properly boot after the firmware flash, the error is reported and subsequent flashing halted, as indicated in block 310. This halting of further flashing is to prevent all modules from becoming inoperable if the image is not stable.

REMARKS

Prior to the initial examination of the application, the Applicants respectfully request that the Examiner enter the amendment to the specification. This amendment is merely to correct the duplicate use of reference number 290 in Figure 4B. Applicants respectfully submit that no new matter is submitted thereby.

Additionally, Applicants submit herewith a proposed drawing amendment, again to correct the duplicate use of reference number 290 in Figure 4B. Applicants submit that this proposed drawing amendment does not present new matter. Applicants respectfully request that the Examiner approve the proposed drawing amendment.

If the Examiner has any questions or comments regarding the proposed corrections, the undersigned may be contacted.

CONCLUSION

If any fees are inadvertently omitted or if any additional fees are required or have been overpaid, please appropriately charge or credit those fees to Conley Rose, P.C. Deposit Account Number 03-2769.

Respectfully submitted,

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APPENDIX

[0039]   Once each microcontroller has a complete copy of the image to be flashed, the initiating server preferably sends a request to a first microcontroller to decode and flash the image as indicated in block 270 of Figure 4B. This request, however, is not a broadcast request; but instead, is a specific request for the first microcontroller to flash the image. If the communication module successfully boots after the image flash as indicated in block 280, the process continues to each subsequent microcontroller in each subsequent communication module, as indicated at block 290. If a particular communication module successfully flashes and boots, and it was the last communication module, as indicated at decision block 29<u>5</u>0, then the process notifies the user that the updates are complete as indicated in block 300, and the process ends. If, however, any communication module fails to properly boot after the firmware flash, the error is reported and subsequent flashing halted, as indicated in block 310. This halting of further flashing is to prevent all modules from becoming inoperable if the image is not stable.